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Attorney's Docket No.: 10559-515001 / P12419

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Eidson et al. Art Unit : Unknown
Serial No. : 09/994,483 Examiner : Unknown
Filed : November 26, 2001
Title : SYSTEM-ON-CHIP BREAKPOINT SYNCHRONIZATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATUS INQUIRY

Applicant respectfully inquires as to the status of the above application. The application was filed on November 26, 2001 and its last known status was receipt of a Notice of Publication of Application mailed 05/29/2003.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 8/5/03

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

August 5, 2003
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